### Project Title

**Design, development and testing of a prototype FPGA**

### Supervisor(s) Name(s) and Department affiliation

**Harpreet Singh, Electrical and Computer Engineering**

### Abstract

This project envisages design, development and testing of a prototype FPGA. The FPGA should be much lower than the costs of FPGA’s available in the market by Xilinx etc. The components of the FPGA could be available as kit in the commercial market. The students will assemble the kit. They will also understand the reasons behind selecting each part and its specification.

### Number of students and background and/or pre-requisites required

The project involve four senior students. They should be motivated to the assembly of electronic components and should have knowledge and interest in building electronic circuits.

### Resources Required

**Electronic components composing the FPGA.**

### Budget Required

$ 200.00